



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: ACHLER Serial No.: 10/026,680 Filed: December 17, 2001 Title: INTEGRATED CIRCUITS FOR A HIGH SPEED ADAPTIVE COMPRESSION AND METHODS THEREFOR	Group Art Unit: 2155 Examiner: ALAM, HOSAIN Docket: ANTAP002/IC-0103 Confirmation No.: 7037
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**INFORMATION DISCLOSURE STATEMENT**

**US PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant	Reference to Related Case

**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date	Applicant	Reference to Related Case	T

**OTHER DOCUMENTS**

Examiner Initials	Cite No.		T
<i>sch</i>		Robert W. Kembel; Fibre Channel: A Comprehensive Introduction", Northwest Learning Associates, Inc., Tucson, AZ 2000	
<i>sch</i>		Mark Nelson; "LZW Data Compression", <a href="http://www.dogma.net/markn/articles/lzw/lzw.htm">http://www.dogma.net/markn/articles/lzw/lzw.htm</a> . Originally published in October 1989 Dr. Dobb's Journal (2800 Campus Drive, San Mateo, CA. <a href="http://www.ddj.com">www.ddj.com</a> )	
<i>sch</i>		Terry A. Welch; "A Technique for High Performance Data Compression", IEEE Computer 17(6), June 1984, pp. 8-19	

Examiner Signature	<i>Shawh. I. I.</i>	Date Considered	2.2.05
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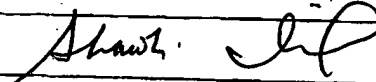
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**OTHER DOCUMENTS**

Examiner Initials	Cite No.	Description	T
sl		US Patent Application No.: 10/026,370 Filed: December 17, 2001 Title: "Architectures for a modularized data optimization engine and methods therefore" Docket: ANTAP001/IC-0102	
sl		US Patent Application No.: 10/026,679 Filed: December 17, 2001 Title: "Interface circuits for modularized data optimization engines and methods therefore" Docket: ANTAP004/IC-0105	
sl		US Patent Application No.: 10/026,368 Filed: December 17, 2001 Title: "Interface receive circuits for modularized data optimization engines and methods therefore" Docket: ANTAP005/IC-0106	

Examiner Signature		Date Considered	2-2-05
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## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	INTEGRATED CIRCUITS FOR HIGH SPEED ADAPTIVE COMPRESSION AND METHODS THEREFOR																																
<p>Application Number: 10/026680</p> <p>Confirmation Number: 7037</p> <p>First Named Applicant: Isaac Achler</p> <p>Attorney Docket Number: ANTAPOO2/IC-0103</p> <p>Art Unit: 2185</p> <p>Search string: ( 5530645 or 5805600 or 6038226 ).pn.</p> <p><b>RECEIVED</b> <b>NOV 17 2004</b> Technology Center 2100</p>																																	
US Patent Documents																																	
Note: Applicant is not required to submit a paper copy of cited US Patent Documents																																	
<table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>11</td><td>1</td><td>5530645</td><td>1996-06-25</td><td>Chu</td><td></td><td></td><td></td></tr><tr><td>12</td><td>2</td><td>5805600</td><td>1998-09-08</td><td>Venters et al.</td><td></td><td></td><td></td></tr><tr><td>13</td><td>3</td><td>6038226</td><td>2000-03-14</td><td>Ellersick et al.</td><td></td><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	11	1	5530645	1996-06-25	Chu				12	2	5805600	1998-09-08	Venters et al.				13	3	6038226	2000-03-14	Ellersick et al.			
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